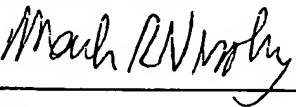
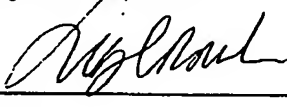
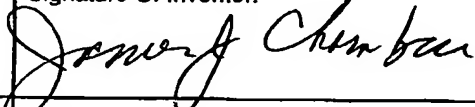


**APPLICATION FOR UNITED STATES PATENT
DECLARATION AND POWER OF ATTORNEY**

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, section 1.56;



I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION: METHOD FOR FABRICATING TRANSISTOR GATE STRUCTURES AND GATE DIELECTRICS THEREOF		
POWER OF ATTORNEY: I HEREBY APPOINT THE FOLLOWING ATTORNEYS TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH. Practitioners at Customer Number: 23494 Thomas G. Eschweiler, Reg. No. 36,981 Eric M. Highman, Reg. No. 43,672 Gregory J. Adams, Reg. No. 44,494 William J. Cooper, Reg. No. 44,629		
SEND CORRESPONDENCE TO: Jacqueline J. Garner Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265		DIRECT TELEPHONE CALLS TO: Jacqueline J. Garner (214) 532-9348
NAME OF INVENTOR: (1) Mark R. Visokay	NAME OF INVENTOR: (2) Luigi Colombo	NAME OF INVENTOR: (3) James J. Chambers
RESIDENCE (City and State Only) Richardson, Texas	RESIDENCE (City and State Only) Dallas, Texas	RESIDENCE (City and State Only) Dallas, Texas
Post Office Address: 2705 Millwood Drive Richardson, Texas 75082	Post Office Address: 6144 Yellow Rock Trail Dallas, Texas 75248	Post Office Address: 4631 TRAM IS ST 6565 McCallum Blvd., #104 #703 Dallas, Texas 75252-75205
Country of Citizenship: United States	Country of Citizenship: United States	Country of Citizenship: United States
Signature of Inventor: 	Signature of Inventor: 	Signature Of Inventor: 
Date: December 8, 2003	Date: December 8, 2003	Date: DEC 8, 2003

APPLICATION FOR UNITED STATES PATENT
DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION: METHOD FOR FABRICATING TRANSISTOR GATE STRUCTURES AND GATE DIELECTRICS THEREOF		
POWER OF ATTORNEY: I HEREBY APPOINT THE FOLLOWING ATTORNEYS TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH. Practitioners at Customer Number: 23494 Thomas G. Eschweiler, Reg. No. 36,981 Eric M. Highman, Reg. No. 43,672 Gregory J. Adams, Reg. No. 44,494 William J. Cooper, Reg. No. 44,629		
SEND CORRESPONDENCE TO: Jacqueline J. Garner Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265		DIRECT TELEPHONE CALLS TO: Jacqueline J. Garner (214) 532-9348
NAME OF INVENTOR: (1) Antonio L.P. Rotondaro	NAME OF INVENTOR: (2) Haowen Bu	NAME OF INVENTOR: (3)
RESIDENCE (City and State Only) Dallas, Texas	RESIDENCE (City and State Only) Plano, Texas	RESIDENCE (City and State Only)
Post Office Address: 6225 Shady Brook Lane, #270 Dallas, Texas 75206	Post Office Address: 6708 Gray Wolf Drive Plano, Texas 75024	Post Office Address:
Country of Citizenship: Brazil	Country of Citizenship: United States	Country of Citizenship:
Signature of Inventor: 	Signature of Inventor: 	Signature Of Inventor:
Date: Dec 08, 2003	Date: 12-10-2003	Date: